

## Glossary

### **1000**

See IRIS 1000

### **2000**

See IRIS 2000

### **3000**

See IRIS 3000

### **4D**

See IRIS 4D

### **6U**

width of 6 unit wide VME bus board

### **9U**

width of 9 unit wide VME bus board

### **AL**

Audio Library

### **ASD**

Advanced System Division - home of VGX, Reality Engine, Onyx and CHALLENGE products

### **ATM**

Asynchronous Transfer Mode

### **BVO**

Broadcast Video Option

### **Blackjack**

Internal name for IP20; Indigo R4000

### **BlueLight**

PI with a 15" 1024x768 display

### **CCIR-601**

Component digital video format; aka D1, aka 4:2:2

**CD-ROM**

Compact Disk - Read Only Memory

**CG2**

Genlock and Composite Video Option (9U) ASD machines only

**CG3**

Genlock and Composite Video Option (6U) ASD and ESD machines

**CISC**

Complex Instruction Set Computer

**Clover1**

IRIS 4D/G; pre GT graphics

**Clover2**

IRIS 4D/GT 4D/GTX graphics

**Crimson**

SGI's first R4000 system from ASD

**Cypress**

Internal name for IRIX 4.0 project

**D1**

Component digital video format

**D2**

Composite digital video format

**DAT**

Digital Audio Tape

**DID**

Display ID, determines pixels display mode

**DMA**

Direct Memory Access

**DSP**

Digital Signal Processing

**Da Vinci**

PI 24-bitplane graphics without Z-buffer

**Diehard**

The code name for the Single Tower chassis

**Diehard2**

The code name for the Crimson chassis

**EEPROM**

Electrically Erasable PROM

**EFS**

Extent File System; the native IRIX file system

**EISA**

Extended Industry Standard Architecture

**EMI**

Electro-Magnetic Interference

**EMR**

Electro-Magnetic Radiation

**EOE**

Execution Only Environment

**EPROM**

Erasable PROM

**EPSF**

Encapsulated PostScript

**ESD**

Electro-Static Discharge, also Entry Systems Division (the birthplace of the Personal IRIS)

**Eclipse**

internal name for the PI

**Elan**

4 GE version of the Express graphics system

**EntryGraphics**

Internal name for Starter Graphics

**EverestMP**

R4000/R4400 system

**Express**

high-performance graphics system for Indigo and 4D/35 which became known as XS, XS24, XZ, Elan and Extreme.

**Extreme**

8 GE version of Express graphics system

**FAQ**

Frequently Asked Question; or summary of answers to FAQ's

**FDDI**

Fiber Distributed Data Interconnect

**Fullhouse**

Indigo 2; SGI machine with EISA bus.

**GE**

Geometry Engine

**GIO**

Graphics I/O Bus

**GL**

Graphics Library

**Guinness**

Former internal name for Indy

**HZ**

Hertz; cycles per second

**Hollywood**

Internal name for Indigo system

**IDB**

Image Database; as in inst idb tags

**IDO**

IRIX Development Option

**IEC**

The International Electrotechnical Commission (Swiss Name)

**IHV**

Independent Hardware Vendor

**IL**

ImageVision Library

**IM**

IRIS InterfaceMaker; Name for SGI's Motif port

**INITTAB**

Initialize Network interfaces, ttys, and bring-up.

**IO1**

Input/Output Board

**IO2**

Input/Output Board

**IO3**

Input/Output Board

**IO4**

Input/Output Board

**IP**

Internet Protocol

**IP**

Iris Processor

**IP**

Instruction Processor

**IP10**

20MHz PI processor (4D/25)

**IP12**

The processor board for the Magnum 4D30 (30MHz) and 4D35 (~37MHz) and the Indigo (30MHz)

**IP13**

2x33MHz R3000A multiprocessor for 4D/300 series

**IP15**

2x40MHz R3000A multiprocessor for 4D/400 series

**IP17**

50MHz R4000 processor board for Crimson (4D/510)

**IP19**

Everest multiprocessor board

**IP20**

50MHz R4000 processor board for Indigo, aka Blackjack

**IP22**

Indigo<sup>2</sup> processor board

**IP24**

Guinness (Indy) Processor Board

**IP26**

Power Indigo<sup>2</sup> (R8000 based Indigo<sup>2</sup>)

**IP27**

Origin2000 and Onyx2 processor

**IP28**

Indigo<sup>2</sup> R10K processor

**IP29**

Origin200 processor

**IP30**

OCTANE processor

**IP32**

O2 processor

**IP4**

12MHz R2000A processor board for 4D/70 (Clover)

**IP4.5**

16.7MHz R2000A processor board for 4D/80

**IP5**

2x16.7MHz R3000[A] multiprocessor board (4D/120)

**IP6**

12.5MHz R3000 PI processor (4D/20)

**IP7**

2x25MHz R3000A multiprocessor for 4D/200 series

**IP9**

25MHz R3000A processor for 4D/210

**IRIS**

Integrated Raster Imaging System: a generic name for Silicon Graphics workstations

**IRIS 1000**

Marketing name for the first Silicon Graphics line of graphics terminals and workstations; Motorola 68010 based

**IRIS 2000**

Marketing name for the second generation of workstations; chief enhancement was a new graphics subsystem; successor to the IRIS 1000 product line

**IRIS 3000**

Marketing name for the last line of workstations that used the Motorola 68000 series processors (specifically the 68020); the successor to the IRIS 2000 product line

**IRIS 4D**

Marketing name for the line of systems that first used the MIPS RISC processor; the successor to the IRIS 3000 product line

**IRIX**

IRIS Unix; SGI's version of ATT System V Unix

**ISDN**

Integrated Services Digital Network; It Still Does Nothing

**ISM**

Inst Selectable Module; any installable software package

**ISV**

Independent Software Vendor

**Indigo**

IRIS Indigo workstation (also, that family of workstations)

**Indy**

SGI's low-end Indigo-family machine

**Inst**

Our IRIX software installation program

**JPEG**

An image compression standard; Joint Photographic Experts Group

**Juniper**

3000 Product Internal Code Name

**kb**

Kilo byte

**LE**

Little Endian

**Lego**

Internal name for Origin2000 and Onyx2 systems

**LG1**

see bluelight

**Ig1**

Starter graphics hardware name, appears in code

**Lonestar**

Internal name for IP17 (Crimson) project

**MCO**

Multi-Channel Option, the VS2

**MG1**

board name for Grinch

**MII**

Panasonic version of Betacam; aka M2

**MIPS**

Million Instructions Per Second

**Moosehead**

Internal name for O2

**MP**

Multi-processor system

**MPEG**

An movie compression standard; Motion Picture Experts Group

**Magnum**

System that used an IP12 processor in a modified PI chassis

**Mirage**

Venice graphics on Everest

**NFS**

Network file system

**NIS**

Network Information Service; Same as YP

**Newport**

graphics hardware for Guinness

**Newpress**

Indigo with 1 Newport head and 1 Express head



**Ng1**

Newport Graphics hardware, appears in code

**OEM**

Original Equipment Manufacturer

**PCI**

Peripheral Component Interconnect, a high speed bus

**pel**

See Pixel

**PIO**

Physical (raw) Input and/or Output

**pixel**

picture element

**PS**

PostScript page description language from Adobe

**Predator**

First rack mounted chassis for 4D series of workstations

**R10K, R10000**

MIPS R10000 processor

**R3K**

MIPS R3000 processor

**R4K**

MIPS R4000 processor, also applies to R4400 and R4600 processors

**R5K**

MIPS R5000 processor

**R8K, R8000**

MIPS R8000 processor

**RE**

RealityEngine

**RE1**

Raster Engine 1; graphics chip in GR1.1 PI

**RE2**

Raster Engine 2; graphics chip in GR1.2 PI

**RGB**

Red Green and Blue color model

**RealityEngine**

Venice graphics hardware

**RISC**

Reduced Instruction Set Computer

**S-VHS**

Super VHS; A consumer/industrial quality video tape format

**S-Video**

a 2-component video format; used by many Hi-8 and S-VHS decks

**sec**

Second

**SGI**

Silicon Graphics, Inc.; the mug and T-shirt company

**SGI**

Soka Gokai International

**SIGGRAPH**

ACM Special Interest Group for Computer Graphics

**SMD**

Storage Module Device

**SNA**

System Network Architecture

**SONET**

Synchronous Optical NETwork; Bellcore's optical transmission interface.

**Speedo**

Internal name for Origin200

**Speedracer**

Internal name for OCTANE

**STREAMS**

AT&T modular device driver interface

**SV1**

Internal designation for IndigoVideo product

**SVID**

System V Interface Description

**SVR4**

System 5 Unix Release 4 from USL

**SW**

Software

**Shamrock**

Clover1

**Sherwood**

IRIX 5.0 based on ATT SVR4 from USL

**SkyWriter**

Dual-headed 10-span VGXT or RealityEngine

**Span**

Vertical columns of pixels i.e., “10-span VGX”

**Stapuft**

Development name for VGX graphics

**Starter**

Lowest cost 1024x768 graphics system for Indigo, aka Entry Graphics

**svideo**

IDB designation for IndigoVideo software

**T5**

Next generation processor project, as far as we know;

**TAC**

Technical Assistance Center. Obsoleted by CSE

**TCP**

Transmission Control Protocol

**TFLU**

Totally Front Loadable Unit, refers to the second generation of Personal IRIS chassis where the main system disk (a 5 1/4” full height unit) was removable from the front of the unit by simply removing a plastic cover.

**TFP**

Twin peaks floating point

**Terminator**

The second generation rack chassis for the 4D series. First shipment with the Onyx and Challenge line

**Top Hat**

The part of the chassis skins that sit on the top of the system. Colors of the top hats often indicated the type of graphics subsystem installed.

**Twin Peaks**

High-performance floating point version of R4000

**Ultra**

8 GE version of the Express graphics system, a.k.a Extreme

**V-LAN**

Video-LAN; a product of Videomedia for controlling video device

**VAD**

Value Added Dealer

**VAR**

Value Added Reseller

**VC**

VideoCreator

**VFR**

VideoFramer

**VFS**

Virtual File System; SUN's file system switch architecture, used by SVR4

**VGX**

1990 highest end graphics machine

**ViewKit**

C++ Application Framework for building Motif Applications

**VINO**

Video in, no out; video chip for Guinness

**VL**

Video library

**VL**

VideoLab

**VLI**

Another name for VideoLab

**VME**

VersaModule Eurocard; 32-bit bus SGI machines use

**VO1**

VideoLab

**VO2**

Next generation VideoLab

**VS1**

VideoSplitter

**VS2**

VideoSplitter2

**VTR**

Video Tape Recorder

**Venice**

1992 highest end graphics machine

**W4D**

Part of Marketing code name for workstation OS

**WORM**

Write Once, Read Mostly

**X**

The X window system

**XS**

1 GE Express with 8-bit graphics

**XS24**

1 GE Express with 24-bit graphics

**XS24Z**

1 GE Express with 24-bit graphics and hardware z-buffer

**XZ**

2 GE Express

**Xsgi**

The Silicon Graphics X Server

**YC**

Luminance-Chrominance; a color system

**YP**

Yellow Pages; A distributed name service now called NIS

**YUV**

A color encoding scheme Y is perceived brightness, U and V are color difference signals